02998.P017

*PATENT*

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: Wolf-Dietrich Weber

Examiner: Not Yet Assigned

Serial No.: 09/977,510

Art Unit: 2185

Filed: October 12, 2001

For: METHOD AND APPARATUS FOR SCHEDULING OF REQUESTS TO A DYNAMIC RANDOM ACCESS MEMORY DEVICE

Assistant Commissioner for Patents
Washington, D.C. 20231

**PRELIMINARY AMENDMENT**

Dear Sir:

Please amend the above-identified application and consider the following remarks.

Please replace paragraph [0003] with the following paragraph:

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**[0003]** The request stream from each different initiator can be described as a thread. If a DRAM scheduler does not re-order requests from the same thread, intra-thread request order is maintained, and the overall DRAM request order is simply an interleaving of the sequential per-thread request streams. This is the definition of Sequential Consistency, the strongest memory ordering model available for systems that include multiple initiator components. (For further discussion regarding Sequential Consistency, see Leslie Lamport, *How to Make a Multiprocessor Computer That Correctly Executes Multiprocess Programs*, IEEE Transactions On Computers, Vol. C-28, No. 9, September 1979, pgs. 690-691.)

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